

| Form PTO-1449 | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY. DOCKET NO. N022-2500 | | SERIAL NO. 10790704 | |
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| LIST OF ART CITED BY APPLICANT (Use several sheets if necessary) | | | | APPLICANT David L. Dickerson et al. | | | |
| | | | | FILING DATE March 31, 2004 | | GROUP 2814 | |
| U.S. PATENT DOCUMENTS | | | | | | | |
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| OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| A.M | AR | K. Ohe et al., "Narrow-Width Effects of Shallow Trench-Isolated CMOS with n+ Polysilicon Gate," IEEE Vol. 36, © 1989, pgs. 1110-1118. | | | | | |
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| EXAMINER ANH D. MAI | | | | DATE CONSIDERED 6/23/05 | | | |
| *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw Box through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |